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formal  
drawings  
PATENT 9.19.02

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THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Protigal et al.

Serial No.: 09/776,387

Filed: February 2, 2001

For: INTEGRATED CIRCUIT MODULE  
HAVING ON-CHIP SURGE CAPACITORS

Examiner: A. Sefer

Group Art Unit: 2826

Attorney Docket No.: 2898.2US  
(88-070.7)

CERTIFICATE OF MAILING

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1.8(a)(i)(ii)

Joseph A. Walkowski  
Typed/printed name of person whose signature is  
contained above

TRANSMITTAL OF FORMAL DRAWINGS

Commissioner for Patents  
Washington, D.C. 20231

Sir:

Attached please find corrected formal drawings for this application.

Respectfully submitted,

Joseph A. Walkowski  
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